

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	86	simulat\$6 near3 test\$5 same list same table\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 13:45
S2	86	simulat\$6 near3 test\$5 same lists same table\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/30 13:35
S3	2	simulat\$6 near3 test\$5 same lists same table\$2 same identifier\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/30 13:37
S4	27	simulat\$6 near3 test\$5 same lists same count\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/30 13:54
S5	1	simulat\$6 near3 test\$5 same design near3 lists same count\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/30 13:51
S6	1	simulat\$6 near3 test\$5 same design near3 list\$2 same count\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/30 13:51
S7	1	simulat\$6 near3 test\$5 same (entity or design) near3 list\$3 same count\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/30 13:54
S8	1	("6470478").PN.	USPAT; USOCR	OR	OFF	2005/07/01 15:27
S9	314554	batch	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/30 15:08
S10	22	batch adj3 farm	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/30 15:08

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S11	23	batch adj3 farm\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/30 15:10
S12	16	batch adj3 farm\$2 same simulat\$6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/30 15:11
S13	2	simulat\$6 same count\$4 same list\$3 same table\$2 same packet	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/30 15:21
S14	155	(703/17).CCLS.	USPAT; USOCR	OR	OFF	2005/06/30 15:21
S15	127	S14 and simulat\$6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/30 15:22
S16	62	S15 and count\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/30 15:22
S17	47	S16 and list\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/30 15:23
S18	48	S16 and list\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/30 15:23
S19	31	S18 and table	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/30 15:23
S20	31	S18 and table\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/30 15:23
S21	0	"6920418"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 13:59

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S22	0	("6920418").PN.	USPAT; USOCR	OR	OFF	2005/07/01 13:59
S23	0	("9752252").PN.	USPAT; USOCR	OR	OFF	2005/07/01 14:03
S24	10	((("5604895") or ("5774380") or ("5840967") or ("5870585") or ("5870588") or ("6195627") or ("6470478") or ("6718520") or ("0070705")).PN.	USPAT; USOCR	OR	OFF	2005/07/01 14:09
S25	1	("5910903").PN.	USPAT; USOCR	OR	OFF	2005/07/01 15:51
S26	2728	(709/203).CCLS.	USPAT; USOCR	OR	OFF	2005/07/01 15:51
S27	256	S26 and simulat\$5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 15:52
S28	2	S26 and simulat\$5 same count	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 16:06
S29	27	S26 and simulat\$5 same list\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 15:58
S30	4	S26 and simulat\$5 same list\$2 same table	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 15:58
S31	0	S26 and simulat\$5 same identifier	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 16:06
S32	1202	S26 and identifier	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 16:06
S33	332	S26 and identifier same table	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 16:06
S34	11	S26 and identifier same table same count	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 16:06
S35	2	((("6470478") or ("6839751")).PN.	USPAT; USOCR	OR	OFF	2006/01/26 18:07

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S36	5	("5604895" "5812416").PN. OR ("6195627").URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/31 11:00
S37	0	simulat\$4 same HDL same identif\$4 same list\$2	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/31 11:01
S38	1214	simulat\$4 same HDL	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/31 11:01
S39	164	simulat\$4 same HDL same list\$2	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/31 11:02
S40	14	simulat\$4 same HDL same list\$2 same identif\$4	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/31 11:10
S41	379	simulat\$4 near2 HDL	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/31 11:10
S42	276	simulat\$4 near2 HDL and identif\$4	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/31 11:10
S43	27	simulat\$4 near2 HDL and identif\$4 and entity near2 list\$2	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/31 11:11
S44	9	("5544067" "5604895" "5812416").PN. OR ("6223142"). URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/31 11:48
S45	0	Bragh.OREF.	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/31 11:50
S46	4	Bargh.OREF.	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/31 11:51
S47	12	Huben.OREF.	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/31 14:49
S48	1214	simulat\$4 same (HDL or harware adj description adj language)	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/31 15:36
S49	167	simulat\$4 same (HDL or harware adj description adj language) same identif\$4	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/31 11:53
S50	14	simulat\$4 same (HDL or harware adj description adj language) same identif\$4 same list\$2	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/31 11:53
S51	15	simulat\$4 same (HDL or harware adj description adj language) same identif\$4 same list\$3	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/31 11:53
S52	0	simulat\$4 same (HDL or harware adj description adj language) same identif\$4 same packet\$2	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/31 11:54
S53	43	simulat\$4 same (HDL or harware adj description adj language) same identif\$4 same event\$2	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/31 11:54
S54	4	simulat\$4 same (HDL or harware adj description adj language) same identif\$4 same event\$2 same list\$2	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/31 11:58

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S55	165	simulat\$4 same (HDL or hardware adj description adj language) and packet\$2	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/31 11:58
S56	140	simulat\$4 same (HDL or hardware adj description adj language) and packet\$2 and identif\$4	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/31 11:58
S57	109	simulat\$4 same (HDL or hardware adj description adj language) and packet\$2 and identif\$4 and list\$2	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/31 11:58
S58	64	simulat\$4 near3 (HDL or hardware adj description adj language) and packet\$2 and identif\$4 and list\$2	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/31 11:59
S59	54	simulat\$4 near2 (HDL or hardware adj description adj language) and packet\$2 and identif\$4 and list\$2	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/31 11:59
S60	19	simulat\$4 near2 (HDL or hardware adj description adj language) and packet\$2 and identif\$4 and list\$2 and farm	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/31 12:07
S61	19	simulat\$4 near2 (HDL or (hardware adj description adj language)) and packet\$2 and identif\$4 and list\$2 and farm	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/31 12:07
S62	10	simulat\$4 near2 (HDL or (hardware adj description adj language)) same farm	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/31 12:08
S63	29	simulat\$4 near2 (HDL or (hardware adj description adj language)) same list	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/31 12:34
S64	379	HDL near2 simulat\$4	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/31 12:35
S65	30	HDL near2 simulat\$4 same identif\$4	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/31 12:35
S66	0	HDL near2 simulat\$4 same identif\$4 same farm	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/31 12:35
S67	2	HDL near2 simulat\$4 same identif\$4 and farm	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/31 12:35
S68	2	HDL near2 simulat\$4 same identif\$4 and farm\$2	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/31 12:36
S69	3	(HDL or (hardware adj description adj (language or program))) near2 simulat\$4 same identif\$4 and farm\$2	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/31 12:37
S70	27	(HDL or (hardware adj description adj (language or program))) near2 simulat\$4 and identif\$4 and farm\$2	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/31 12:45
S71	1111	simulat\$4 same identif\$4 same list\$2	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/31 12:45
S72	2	simulat\$4 same identif\$4 same list\$2 same farm	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/31 12:46
S73	2	simulat\$4 same identif\$4 same list\$2 same farm\$2	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/31 12:46

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S74	93	simulat\$4 same identif\$4 same list\$2 and farm\$2	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/31 12:46
S75	29	simulat\$4 same identif\$4 same list\$2 and farm\$2 and HDL	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/31 12:48
S76	22	test\$3 same HDL same ident\$6 same list\$2	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/31 12:49
S77	4325	simulat\$4 and farm\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/31 12:57
S78	397	simulat\$4 same farm\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/31 12:57
S79	33	simulat\$4 same farm\$2 and HDL	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/31 12:57
S80	24	simulat\$4 same farm\$2 same HDL	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/31 12:58
S81	1	("6839751").PN.	USPAT; USOCR	OR	OFF	2006/01/31 16:39
S82	7	Haben.OREF.	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/31 14:50
S83	1	HDL same identifier\$2 same entity near list\$2	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/31 14:50
S84	14	HDL same identifier\$2 same list\$2	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/31 15:01
S85	13	simulat\$4 same HDL same count\$2 same list\$2	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/31 15:01
S86	3	("6360335" "6539503" "6560720").PN. OR ("6934885"). URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/31 15:02
S87	66	simulat\$4 same (HDL or hardware adj description adj language) same count\$2	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/31 15:37
S88	21	simulat\$4 same (HDL or hardware adj description adj language) same count\$2 same identif\$4	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/31 15:37

EAST Search History

S89	0	aimulat\$4 near farm\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/31 15:59
S90	66	simulat\$4 near2 farm\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/31 15:59
S91	349	simulat\$4 near client\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/31 16:40
S92	15	simulat\$4 near client\$2 same farm	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/31 16:40
S93	18529565	"6,785,773" "6,732,338" "6,678,643" "6,651,204" "6,560,721" "6,560,720" "6,539,503" "6,516,342" "6,484,135" "6,466,898" "6,360,335" US "6,353,897" 03-05-2002 NOCK ET "AL." US "6,336,087" 01-01-2002 BURGUN ET "AL." US "6,292,909" 09-18-2001 HARE, DUNCAN US "6,223,142" 04-24-2001 BARGH ET "AL." US "6,195,629" "6.195,627" "6,163,763" "6,065,046" "6.011,920" "5,850,345" "5,604,895" "5,467,462" "5,403,639" "4,914,612"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/31 18:56
S94	16	((("6,785,773") or ("6,732,338") or ("6,678,643") or ("6,651,204") or ("6,560,721") or ("6,560,720") or ("6,539,503") or ("6,516,342", "6,484,135") or ("6,466,898") or ("6,360,335") or ("US"6,353,897"03-05-2002NOCKET"AL.") or ("US"6,336,087"01-01-2002BURGUNET"AL.") or ("US"6,292,909"09-18-2001HARE,DUNCAN") or ("US"6,223,142"04-24-2001BARGHET"AL.") or ("US"6,195,629""6.195,627") or ("6,163,763") or ("6,065,046") or ("6.011,920") or ("5,850,345") or ("5,604,895") or ("5,467,462") or ("5,403,639") or ("4,914,612"))).PN.	USPAT; USOCR	OR	OFF	2006/01/31 19:06
S95	176	(703/17).CCLS.	USPAT; USOCR	OR	OFF	2006/01/31 19:06
S96	38	S95 and HDL	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/31 19:07
S97	38	S95 and HDL and simulat\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/31 19:10
S98	30	S95 and HDL and simulat\$4 and identif\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/31 19:10

EAST Search History

S99	25	S95 and HDL and simulat\$4 and identif\$4 and list\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/31 19:11
S100	1	("6839751").PN.	USPAT; USOCR	OR	OFF	2006/02/01 11:01
S101	1	("6950909").PN.	USPAT; USOCR	OR	OFF	2006/03/27 10:21
S102	4	("6118608" "6434668" "6442653" "6687795").PN. OR ("6950909").URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2006/03/27 10:56
S103	8	((("7027971") or ("6978231") or ("6920418") or ("6941257") or ("6223142") or ("6212491") or ("0647078") or ("6195627"))).PN.	USPAT; USOCR	OR	OFF	2006/03/27 14:11
S104	1370	((703/17) or (703/22) or (703/13) or (703/21)).CCLS.	USPAT; USOCR	OR	OFF	2006/03/27 14:11
S105	1038	S104 and simulat\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/27 14:12
S106	16	S105 and (count near event)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/27 14:16
S107	2	S106 and packet\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/27 14:20
S108	15	S106 and list\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/27 14:20
S109	2	S106 and list\$2 and packet\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/27 14:22
S110	114	S105 and packet\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/27 14:22
S111	0	S105 and packet\$2 and entity adj list@	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/27 14:22

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S11 2	0	S105 and packet\$2 and entity adj list\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/27 14:23
S11 3	88	S105 and packet\$2 and test\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/27 14:23
S11 4	11	S105 and packet\$2 and test\$3 and HDL	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/27 14:56
S11 5	3	S104 and (HDL same simulat\$4) and (count same packet)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/27 14:59
S11 6	5153	access\$3 same count\$2 same event	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/27 14:59
S11 7	13	access\$3 same count\$2 same event near packet\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/27 15:13
S11 8	777	702/108	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/27 15:13
S11 9	1222	702/117	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/27 15:13
S12 0	2274	((702/108) or (702/117) or (702/118) or (716/4)).CCLS.	USPAT; USOCR	OR	OFF	2006/03/27 15:14
S12 1	17	S120 and (event near count)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/27 15:34
S12 2	258	S120 and HDL	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/27 15:34

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S12 3	6	S120 and HDL and (count near2 event\$2)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/27 15:34
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Day : Monday
Date: 3/27/2006

Time: 10:26:41

PALM INTRANET

Inventor Name Search Result

Your Search was:

Last Name = WILLIAMS

First Name = DEREK

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>10749759</u>	Not Issued	93	12/31/2003	METHOD, SYSTEM AND PROGRAM PRODUCT PROVIDING A CONFIGURATION SPECIFICATION LANGUAGE SUPPORTING ARBITRARY MAPPING FUNCTIONS FOR CONFIGURATION CONSTRUCTS	WILLIAMS, DEREK EDWARD
<u>10750112</u>	Not Issued	95	12/31/2003	METHOD, SYSTEM AND PROGRAM PRODUCT FOR SPECIFYING AND USING DIALS HAVING PHASED DEFAULT VALUES TO CONFIGURE A SIMULATED OR PHYSICAL DIGITAL SYSTEM	WILLIAMS, DEREK EDWARD
<u>10750590</u>	Not Issued	20	12/31/2003	Method, system and program product supporting user tracing in a simulator	WILLIAMS, DEREK EDWARD
<u>10825188</u>	Not Issued	30	04/15/2004	Method to stall store operations to increase chances of gathering full entries for updating cachelines	WILLIAMS, DEREK EDWARD
<u>10825189</u>	Not Issued	30	04/15/2004	Method for completing full cacheline stores with address-only bus operations	WILLIAMS, DEREK EDWARD
<u>10857461</u>	Not Issued	30	05/28/2004	Method, system and program product for specifying and using register entities to configure a simulated or physical digital system	WILLIAMS, DEREK EDWARD
<u>10880730</u>	Not Issued	20	06/30/2004	Methods, systems and program products for annotating system traces with control program	WILLIAMS, DEREK EDWARD

				information and presenting annotated system traces	
<u>10880853</u>	Not Issued	20	06/30/2004	Method, system and program product for providing a configuration specification language supporting incompletely specified configuration entities	WILLIAMS, DEREK EDWARD
<u>10902595</u>	Not Issued	30	07/29/2004	Method, system and program product for providing a configuration specification language supporting selective presentation of configuration entities	WILLIAMS, DEREK EDWARD
<u>10902628</u>	Not Issued	20	07/29/2004	Method, system and program product supporting presentation of a simulated or hardware system including configuration entities	WILLIAMS, DEREK EDWARD
<u>10922272</u>	Not Issued	30	08/19/2004	Processor, method, and data processing system employing a variable store gather window	WILLIAMS, DEREK EDWARD
<u>10965113</u>	Not Issued	30	10/14/2004	Processor, data processing system and method for synchronizing access to data in shared memory	WILLIAMS, DEREK EDWARD
<u>10965144</u>	Not Issued	30	10/14/2004	Processor, data processing system and method for synchronizing access to data in shared memory	WILLIAMS, DEREK EDWARD
<u>10965151</u>	Not Issued	30	10/14/2004	Processor, data processing system and method for synchronizing access to data in shared memory	WILLIAMS, DEREK EDWARD
<u>10970437</u>	Not Issued	30	10/21/2004	Method for priority scheduling and priority dispatching of store conditional operations in a store queue	WILLIAMS, DEREK EDWARD
<u>10970468</u>	Not Issued	20	10/21/2004	Method, system and program product for defining and recording minimum and maximum count events of a simulation	WILLIAMS, DEREK EDWARD
<u>10970469</u>	Not Issued	20	10/21/2004	Method, system and program product for defining and recording minimum and maximum event counts of a simulation utilizing a high level language	WILLIAMS, DEREK EDWARD
<u>11006093</u>	Not Issued	20	12/07/2004	Method, system and program product for defining and recording threshold-qualified count events of a simulation by testcases	WILLIAMS, DEREK EDWARD

<u>11054450</u>	Not Issued	30	02/09/2005	System and method of re-ordering store operations within a processor	WILLIAMS, DEREK EDWARD
<u>11055301</u>	Not Issued	30	02/10/2005	Data processing system and method for efficient L3 cache directory management	WILLIAMS, DEREK EDWARD
<u>11055476</u>	Not Issued	30	02/10/2005	Data processing system and method for efficient communication utilizing an Tn and Ten coherency states	WILLIAMS, DEREK EDWARD
<u>11056673</u>	Not Issued	30	02/12/2005	Cache memory direct intervention	WILLIAMS, DEREK EDWARD
<u>11109085</u>	Not Issued	30	04/19/2005	Cache memory, processing unit, data processing system and method for assuming a selected invalid coherency state based upon a request source	WILLIAMS, DEREK EDWARD
<u>11245312</u>	Not Issued	30	10/06/2005	Chained cache coherency states for sequential non-homogeneous access to a cache line with outstanding data response	WILLIAMS, DEREK EDWARD
<u>11245313</u>	Not Issued	30	10/06/2005	Chained cache coherency states for sequential homogeneous access to a cache line with outstanding data response	WILLIAMS, DEREK EDWARD
<u>08815647</u>	<u>5895495</u>	150	03/13/1997	DEMAND-BASED LARX-RESERVE PROTOCOL FOR SMP SYSTEM BUSES	WILLIAMS, DEREK EDWARD
<u>08815648</u>	<u>6029204</u>	150	03/13/1997	PRECISE SYNCHRONIZATION MECHANISM FOR SMP SYSTEM BUSES USING TAGGED SNOOP OPERATIONS TO AVOID RETRIES	WILLIAMS, DEREK EDWARD
<u>08829577</u>	<u>6212605</u>	250	03/31/1997	EVICTON OVERRIDE FOR LARX-RESERVED ADDRESSES	WILLIAMS, DEREK EDWARD
<u>08834113</u>	<u>6173371</u>	150	04/14/1997	DEMAND-BASED ISSUANCE OF CACHE OPERATIONS TO A PROCESSOR BUS	WILLIAMS, DEREK EDWARD
<u>08834114</u>	<u>5978871</u>	150	04/14/1997	METHOD OF LAYERING CACHE AND ARCHITECTURAL SPECIFIC FUNCTIONS FOR OPERATION SPLITTING	WILLIAMS, DEREK EDWARD
<u>08834115</u>	<u>6032226</u>	150	04/14/1997	METHOD AND APPARATUS	WILLIAMS,

				FOR LAYERING CACHE AND ARCHITECTURAL SPECIFIC FUCTIONS TO EXPEDITE MULTIPLE DESIGN	DEREK EDWARD
<u>08834116</u>	<u>6182201</u>	150	04/14/1997	DEMAND-BASED ISSUANCE OF CACHE OPERATIONS TO A SYSTEM BUS	WILLIAMS, DEREK EDWARD
<u>08834120</u>	<u>6105112</u>	150	04/14/1997	DYNAMIC FOLDING OF CACHE OPERATIONS FOR MULTIPLE COHERENCY-SIZE SYSTEMS	WILLIAMS, DEREK EDWARD
<u>08839436</u>	<u>5935234</u>	150	04/14/1997	METHOD AND SYSTEM FOR CONTROLLING ACCESS TO A SHARED RESOURCE IN A DATA PROCESSING SYSTEM UTILIZING PSEUDO-RANDOM PRIORITIES	WILLIAMS, DEREK EDWARD
<u>08839437</u>	<u>5931924</u>	150	04/14/1997	METHOD AND SYSTEM FOR CONTROLLING ACCESS TO A SHARED RESOURCE THAT EACH REQUESTOR IS CONCURRENTLY ASSIGNED AT LEAST TWO PSEUDO-RANDOM PRIORITY WEIGHTS	WILLIAMS, DEREK EDWARD
<u>08839438</u>	<u>5896539</u>	150	04/14/1997	METHOD AND SYSTEM FOR CONTROLLING ACCESS TO A SHARED RESOURCE IN A DATA PROCESSING SYSTEM UTILIZING DYNAMICALLY-DETERMINED WEIGHTED PSEUDO-RANDOM PRIORITIES	WILLIAMS, DEREK EDWARD
<u>08839441</u>	<u>6061755</u>	150	04/14/1997	METHOD OF LAYERING CACHE AND ARCHITECTURAL SPECIFIC FUNCTIONS TO PROMOTE OPERATION SYMMETRY	WILLIAMS, DEREK EDWARD
<u>08839442</u>	<u>6061762</u>	150	04/14/1997	APPARATUS AND METHOD FOR SEPARATELY LAYERING CACHE AND ARCHITECTURAL SPECIFIC FUNCTIONS IN DIFFERENT OPERATIONAL CONTROLLERS	WILLIAMS, DEREK EDWARD
<u>08839443</u>	<u>5909561</u>	150	04/14/1997	APPARATUS AND METHOD FOR SEPARATELY LAYERING	WILLIAMS, DEREK EDWARD

				CACHE AND ARCHITECTURAL SPECIFIC FUNCTIONS IN DIFFERENT OPERATIONAL CONTROLLERS TO FACILITATE DESIGN EXTENSION	
<u>08839445</u>	<u>5937172</u>	150	04/14/1997	APPARATUS AND METHOD OF LAYERING CACHE AND ARCHITECTURAL SPECIFIC FUNCTIONS TO PERMIT GENERIC INTERFACE DEFINITION	WILLIAMS, DEREK EDWARD
<u>08934407</u>	<u>5951668</u>	150	09/19/1997	METHOD AND SYSTEM FOR TRANSFERRING DATA BETWEEN BUSES HAVING DIFFERING ORDERING POLICIES	WILLIAMS, DEREK EDWARD
<u>08934413</u>	<u>5901299</u>	150	09/19/1997	METHOD AND APPARATUS FOR TRANSFERRING DATA BETWEEN BUSES HAVING DIFFERING ORDERING POLICIES	WILLIAMS, DEREK EDWARD
<u>08934414</u>	<u>5938753</u>	150	09/19/1997	METHOD AND APPARATUS FOR CONTROLLING AUTONOMOUS UNITS TRANSFERRING DATA BETWEEN BUSES HAVING DIFFERENT ORDERING POLICIES	WILLIAMS, DEREK EDWARD
<u>08934415</u>	<u>6016526</u>	150	09/19/1997	METHOD AND APPARATUS FOR TRANSFERRING DATA BETWEEN BUSES HAVING DIFFERING ORDERING POLICIES VIA THE USE OF AUTONOMOUS UNITS	WILLIAMS, DEREK EDWARD
<u>08992132</u>	<u>6161189</u>	150	12/17/1997	Latch-And-Hold Circuit That Permits Subcircuits Of An Integrated Circuit To Operate At Different Frequencies	WILLIAMS, DEREK EDWARD
<u>09004137</u>	Not Issued	161	01/07/1998	METHOD AND APPARATUS FOR EXECUTING MULTIPLY-INITIATED, SINGLY-SOURCED VARIABLE DELAY SYSTEM BUS OPERATIONS	WILLIAMS, DEREK EDWARD
<u>09004145</u>	<u>6014721</u>	150	01/07/1998	METHOD AND SYSTEM FOR TRANSFERRING DATA	WILLIAMS, DEREK EDWARD

				BETWEEN BUSES HAVING DIFFERING ORDERING POLICIES	
09004147	6202131	250	01/07/1998	METHOD AND APPARATUS FOR EXECUTING VARIABLE DELAY SYSTEM BUS OPERATIONS OF DIFFERING TYPE OF CHARACTER WITHOUT DEAD LOCK USING SHARED BUFFERS	WILLIAMS, DEREK EDWARD
09004148	6128705	250	01/07/1998	METHOD AND APPARATUS FOR EXECUTING MULTIPLY- INITIATED, MULTIPLY- SOURCED VARIABLE DELAY SYSTEM BUS OPERATIONS	WILLIAMS, DEREK EDWARD
09004149	Not Issued	161	01/07/1998	METHOD AND APPARATUS FOR EXECUTING SINGLY- INITIATED, SINGLY- SOURCED VARIABLE DELAY SYSTEM BUS OPERATIONS	WILLIAMS, DEREK EDWARD

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Inventor Name Search Result

Your Search was:

Last Name = ROESNER

First Name = WOLFGANG

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>10388976</u>	Not Issued	30	03/13/2003	Method and system for reducing storage requirements of simulation data via keyword restrictions	ROESNER, WOLFGANG
<u>10425041</u>	Not Issued	30	04/28/2003	Method, system and program product for configuring a simulation model of a digital design	ROESNER, WOLFGANG
<u>10425051</u>	Not Issued	30	04/28/2003	Method, system and program product for specifying a configuration for multiple signal or dial instances in a digital system	ROESNER, WOLFGANG
<u>10425053</u>	Not Issued	30	04/28/2003	Method, system and program product for determining a configuration of a digital design by reference to an invertible configuration database	ROESNER, WOLFGANG
<u>10425070</u>	6993729	150	04/28/2003	METHOD, SYSTEM AND PROGRAM PRODUCT FOR SPECIFYING A DIAL GROUP FOR A DIGITAL SYSTEM DESCRIBED BY A HARDWARE DESCRIPTION LANGUAGE (HDL) MODEL	ROESNER, WOLFGANG
<u>10425072</u>	6941527	150	04/28/2003	METHOD, SYSTEM AND PROGRAM PRODUCT FOR REDUCING A SIZE OF A CONFIGURATION DATABASE UTILIZED TO CONFIGURE A HARDWARE DIGITAL SYSTEM	ROESNER, WOLFGANG
<u>10425074</u>	Not Issued	93	04/28/2003	METHOD, SYSTEM AND PROGRAM PRODUCT FOR	ROESNER, WOLFGANG

				AUTOMATICALLY TRANSFORMING A CONFIGURATION OF A DIGITAL SYSTEM UTILIZING TRACEBACK OF SIGNAL STATES	
<u>10425075</u>	<u>6826732</u>	150	04/28/2003	METHOD, SYSTEM AND PROGRAM PRODUCT FOR UTILIZING A CONFIGURATION DATABASE TO CONFIGURE A HARDWARE DIGITAL SYSTEM	ROESNER, WOLFGANG
<u>10425076</u>	Not Issued	25	04/28/2003	Method, system and program product that utilize a configuration database to configure a hardware digital system having an arbitrary system size and component set	ROESNER, WOLFGANG
<u>10425079</u>	Not Issued	93	04/28/2003	METHOD, SYSTEM AND PROGRAM PRODUCT FOR SPECIFYING AND USING A DIAL HAVING A DEFAULT VALUE TO CONFIGURE A DIGITAL SYSTEM DESCRIBED BY A HARDWARE DESCRIPTION LANGUAGE (HDL) MODEL	ROESNER, WOLFGANG
<u>10425080</u>	Not Issued	30	04/28/2003	Method, system and program product for implementing a read- only dial in a configuration database of a digital design	ROESNER, WOLFGANG
<u>10425089</u>	Not Issued	93	04/28/2003	METHOD, SYSTEM AND PROGRAM PRODUCT THAT UTILIZE A CONFIGURATION DATABASE TO CONFIGURE A HARDWARE DIGITAL SYSTEM HAVING MULTIPLE ACCESS METHODS	ROESNER, WOLFGANG
<u>10425096</u>	Not Issued	93	04/28/2003	METHOD, SYSTEM AND PROGRAM PRODUCT FOR SPECIFYING A CONFIGURATION OF A DIGITAL SYSTEM DESCRIBED BY A HARDWARE DESCRIPTION LANGUAGE (HDL) MODEL	ROESNER, WOLFGANG
<u>10651156</u>	Not	30	08/28/2003	Method, system and program	ROESNER,

	Issued			product that automatically generate coverage instrumentation for configuration constructs within a digital system	WOLFGANG
<u>10651186</u>	Not Issued	30	08/28/2003	Method, system and program product providing a configuration specification language having clone latch support	ROESNER, WOLFGANG
<u>10651187</u>	Not Issued	41	08/28/2003	Method, system and program product providing a configuration specification language that supports the definition of links between configuration constructs	ROESNER, WOLFGANG
<u>10749581</u>	Not Issued	30	12/31/2003	Method, system and program product providing a configuration specification language having split latch support	ROESNER, WOLFGANG
<u>10749607</u>	Not Issued	20	12/31/2003	Method and system for selective compilation of instrumentation entities into a simulation model of a digital design	ROESNER, WOLFGANG
<u>10749759</u>	Not Issued	93	12/31/2003	METHOD, SYSTEM AND PROGRAM PRODUCT PROVIDING A CONFIGURATION SPECIFICATION LANGUAGE SUPPORTING ARBITRARY MAPPING FUNCTIONS FOR CONFIGURATION CONSTRUCTS	ROESNER, WOLFGANG
<u>10750112</u>	Not Issued	95	12/31/2003	METHOD, SYSTEM AND PROGRAM PRODUCT FOR SPECIFYING AND USING DIALS HAVING PHASED DEFAULT VALUES TO CONFIGURE A SIMULATED OR PHYSICAL DIGITAL SYSTEM	ROESNER, WOLFGANG
<u>10750590</u>	Not Issued	20	12/31/2003	Method, system and program product supporting user tracing in a simulator	ROESNER, WOLFGANG
<u>10768971</u>	Not Issued	61	01/30/2004	Fin field-effect transistor and method for producing a fin field effect-transistor	ROESNER, WOLFGANG
<u>10857461</u>	Not	30	05/28/2004	Method, system and program	ROESNER,

	Issued			product for specifying and using register entities to configure a simulated or physical digital system	WOLFGANG
<u>10880730</u>	Not Issued	20	06/30/2004	Methods, systems and program products for annotating system traces with control program information and presenting annotated system traces	ROESNER, WOLFGANG
<u>10880853</u>	Not Issued	20	06/30/2004	Method, system and program product for providing a configuration specification language supporting incompletely specified configuration entities	ROESNER, WOLFGANG
<u>10902595</u>	Not Issued	30	07/29/2004	Method, system and program product for providing a configuration specification language supporting selective presentation of configuration entities	ROESNER, WOLFGANG
<u>10902628</u>	Not Issued	20	07/29/2004	Method, system and program product supporting presentation of a simulated or hardware system including configuration entities	ROESNER, WOLFGANG
<u>10939319</u>	Not Issued	30	09/09/2004	Vertical impedance sensor arrangement and method for producing a vertical impedance sensor arrangement	ROESNER, WOLFGANG
<u>10968846</u>	Not Issued	61	10/18/2004	Substrate and method for producing a substrate	ROESNER, WOLFGANG
<u>10970468</u>	Not Issued	20	10/21/2004	Method, system and program product for defining and recording minimum and maximum count events of a simulation	ROESNER, WOLFGANG
<u>10970469</u>	Not Issued	20	10/21/2004	Method, system and program product for defining and recording minimum and maximum event counts of a simulation utilizing a high level language	ROESNER, WOLFGANG
<u>11000350</u>	Not Issued	30	11/30/2004	Charge-trapping memory cell and method for production	ROESNER, WOLFGANG
<u>11006093</u>	Not Issued	20	12/07/2004	Method, system and program product for defining and recording threshold-qualified count events of a simulation by	ROESNER, WOLFGANG

				testcases	
<u>11050232</u>	Not Issued	30	02/03/2005	Method for reconfiguration of random biases in a synthesized design without recompilation	ROESNER, WOLFGANG
<u>11054880</u>	Not Issued	20	02/10/2005	System and method for accurately modeling an asynchronous interface using expanded logic elements	ROESNER, WOLFGANG
<u>11055863</u>	Not Issued	30	02/11/2005	Method and apparatus for specifying multiple voltage domains and validating physical implementation and interconnections in a processor chip	ROESNER, WOLFGANG
<u>11073017</u>	Not Issued	41	03/04/2005	High-density NROM-FINFET	ROESNER, WOLFGANG
<u>11116139</u>	Not Issued	30	04/27/2005	Integrated circuit array	ROESNER, WOLFGANG
<u>11119531</u>	Not Issued	30	04/29/2005	Memory cell, memory cell arrangement, patterning arrangement, and method for fabricating a memory cell	ROESNER, WOLFGANG
<u>11175912</u>	Not Issued	20	07/05/2005	Layer arrangement and process for producing a layer arrangement	ROESNER, WOLFGANG
<u>11177235</u>	Not Issued	30	07/07/2005	Planar dual-gate transistor and method for fabricating a planar dual-gate transistor	ROESNER, WOLFGANG
<u>11178251</u>	Not Issued	30	07/08/2005	Process for producing a layer arrangement, and layer arrangement for use as a dual-gate field-effect transistor	ROESNER, WOLFGANG
<u>11193026</u>	Not Issued	30	07/29/2005	Semiconductor memory, the fabrication thereof and a method for operating the semiconductor memory	ROESNER, WOLFGANG
<u>11226969</u>	Not Issued	20	09/15/2005	Method, system and program product for selectively removing instrumentation logic from a simulation model	ROESNER, WOLFGANG
<u>11245300</u>	Not Issued	25	10/06/2005	Method, system and program product for providing a configuration specification language supporting error checking dials	ROESNER, WOLFGANG
<u>11324803</u>	Not	19	01/03/2006	Integrated circuit arrangement	ROESNER,

	Issued			with low-resistance contacts and method for production thereof	WOLFGANG
<u>11345847</u>	Not Issued	20	02/02/2006	Method, system and program product for specifying a configuration for a digital system utilizing dial biasing weights	ROESNER, WOLFGANG

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Inventor Name Search Result

Your Search was:

Last Name = GABELE

First Name = CAROL

Application#	Patent#	Status	Date Filed	Title	Inventor Name
09997458	Not Issued	61	11/30/2001	Non-redundant collection of harvest events within a batch simulation farm network	GABELE, CAROL IVASH
09997460	Not Issued	20	11/30/2001	Tracking convergence results in a batch simulation farm network	GABELE, CAROL IVASH
09997767	6934885	150	11/30/2001	FAIL THRESHOLDING IN A BATCH SIMULATION FARM NETWORK	GABELE, CAROL IVASH
09997768	7027971	150	11/30/2001	CENTRALIZED DISABLEMENT OF INSTRUMENTATION EVENTS WITHIN A BATCH SIMULATION FARM NETWORK	GABELE, CAROL IVASH
09997802	Not Issued	41	11/30/2001	Maintaining data integrity within a distributed simulation environment	GABELE, CAROL IVASH
09997803	Not Issued	80	11/30/2001	Count data access in a distributed simulation environment	GABELE, CAROL IVASH
09997845	Not Issued	41	11/30/2001	Annealing harvest event testcase collection within a batch simulation farm	GABELE, CAROL IVASH
10116521	Not Issued	61	04/04/2002	Dynamic loading of C-API HDL model instrumentation	GABELE, CAROL IVASH
10116524	Not Issued	71	04/04/2002	C-API instrumentation for HDL models	GABELE, CAROL IVASH
10355684	Not Issued	30	01/30/2003	Method and system for reducing storage and transmission requirements for simulation results	GABELE, CAROL IVASH
10366438	Not Issued	40	02/13/2003	Method and system for selectively storing and retrieving simulation data utilizing keywords	GABELE, CAROL IVASH
10388976	Not	30	03/13/2003	Method and system for reducing	GABELE, CAROL

	Issued			storage requirements of simulation data via keyword restrictions	IVASH
<u>10970468</u>	Not Issued	20	10/21/2004	Method, system and program product for defining and recording minimum and maximum count events of a simulation	GABELE, CAROL IVASH
<u>10970469</u>	Not Issued	20	10/21/2004	Method, system and program product for defining and recording minium and maximum event counts of a simulation utilizing a high level language	GABELE, CAROL IVASH
<u>11006093</u>	Not Issued	20	12/07/2004	Method, system and program product for defining and recording threshold-qualified count events of a simulation by testcases	GABELE, CAROL IVASH
<u>08829578</u>	<u>5991521</u>	150	03/31/1997	METHOD AND SYSTEM OF CHECKING FOR OPEN CIRCUIT CONNECTIONS WITHIN AN INTEGRATED-CIRCUIT DESIGN REPRESENTED BY A HIERARCHICAL DATA STRUCTURE	GABELE, CAROL IVASH
<u>08944975</u>	<u>6360350</u>	150	10/07/1997	METHOD AND SYSTEM FOR PERFORMING CIRCUIT ANALYSIS ON AN INTEGRATED-CIRCUIT DESIGN HAVING DESIGN DATA AVAILABLE IN DIFFERENT FORMS	GABELE, CAROL IVASH
<u>09119271</u>	<u>6230302</u>	150	07/20/1998	METHOD AND SYSTEM FOR PERFORMING TIMING ANALYSIS ON AN INTEGRATED CIRCUIT DESIGN	GABELE, CAROL IVASH

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